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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/432,855	11/02/1999	DESMOND E. WONG	0100.9901360	1672

29153 7590 03/08/2006

ATI TECHNOLOGIES, INC.
C/O VEDDER PRICE KAUFMAN & KAMMHOLZ, P.C.
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CHICAGO, IL 60601

EXAMINER

SHANKAR, VIJAY

ART UNIT PAPER NUMBER

2673

DATE MAILED: 03/08/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/432,855	WONG, DESMOND E.	
	Examiner	Art Unit	
	VIJAY SHANKAR	2673	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 19 September 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) 7-9 and 14-22 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-6, 10-13 and 23-27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-6, 10-13, and 23-27 are rejected under 35 U.S.C. 102(e) as being anticipated by Apostol, Jr. et al (6,247,084).

Regarding Claim 1, Apostol, Jr. et al a method for detecting a monitor, the method comprising: monitoring one pin of a connector coupled to a flat panel display (Figs. 1-5; Col.6, line 1-65; Col.30, line 47- Col.31, line 65); asserting an output signal to indicate the one pin is in a first state; and receiving the output signal at a display engine. (Figs. 1-5; Col.5, line 45- Col.7, line 40; Col.30, line 47- Col.31, line 65).

Regarding Claims 2-4, 24, 26, Apostol, Jr. et al a method wherein the output signal is an interrupt signal, the interrupt signal is a system interrupt for a general purpose computer, and the output signal is stored in a register. (Col.6, line 1-65; Col.19, line 39- Col.21, line 25).

Regarding Claims 5-6, 27, Apostol, Jr. et al the method further comprising determining if a voltage level of the one pin is in a stable state before asserting the output signal; and determining includes the voltage level of the one pin being stable when the input is stable for a predetermined amount of time. (Col.6, line 1-65; Col.19, line 39- Col.21, line 25).

Regarding Claims 10-13, Apostol, Jr. et al the method further comprising the step of: operating in a normal mode of operation prior to monitoring, wherein the one pin is in a second state, and the first state is indicative of a flat panel display being coupled and decoupled to the connector, and driving the flat panel display from the flat panel display engine in response to asserting the first output signal. (Figs. 1-5; Col.5, line 45- Col.7, line 40; Col.30, line 47- Col.31, line 65).

Regarding Claim 23, Apostol, Jr. et al a system for providing a display image to a flat panel monitor, the system comprising: a processing module; and memory operably coupled to the processing module, wherein in the memory stores operational instructions that cause the processing module to monitor one pin of a connector coupled to a flat panel display (Figs. 1-5; Col.5, line 45- Col.7, line 40; Col.30, line 47- Col.31, line 65); assert a output signal to indicate the one pin is in a first state; and receive the output signal at a display engine. (Figs. 1-5; Col.5, line 45- Col.7, line 40; Col.30, line 47- Col.31, line 65).

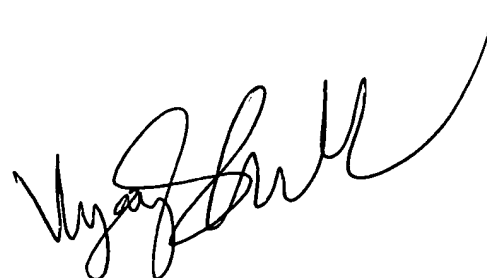
Regarding Claim 25, Apostol, Jr. et al the method for detecting a monitor, the method comprising: providing display information to a first display, determining when an external flat panel display becomes available, by monitoring at least one pin of a connector coupled to a flat panel display (Figs. 1-5; Col.5, line 45- Col.7, line 40; Col.30, line 47- Col.31, line 65); asserting an output signal to indicate the pin is in a first state; providing an interrupt signal in response to the asserted output signal (Col.6, line 1-65; Col.19, line 39- Col.21, line 25), and providing display information to the external flat panel display in response to the interrupt signal. (Figs. 1-5; Col.5, line 45- Col.7, line 40; Col.30, line 47- Col.31, line 65).

3. Applicant's arguments with respect to claims 1-6, 10-13, 23-27 have been considered but are moot in view of the new ground(s) of rejection.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



VIJAY SHANKAR
Primary Examiner
Art Unit 2673

VS